

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Moore et al.

Serial No.: 09/703,181

Filed: October 30, 2000

Title: Architecture for Efficient
Implementation of Serial Data
Communication Functions On a
Programmable Logic Device (PLD)

Group Art Unit: 2825

Examiner: Liu, A.

Attorney Docket No.: CY-0016

RESPONSE TO OFFICE ACTION

5 Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

10 The following is submitted in response to the Office Action dated December 3, 2002, and
is currently due March 3, 2003.

Entry of the following amendments is respectfully requested.

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37 C.F.R. §1.8

I hereby certify that this correspondence is being

25 ☒ transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-703-872-9318
Date of Transmittal: MARCH 2, 200330 ☐ deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for
Patents, Washington, D.C. 20231.

Date of Deposit: _____

35 Typed/Printed Name: BRADLEY T. SAKOSignature: *Bradley T. Sako*